

WNSQ-290BE(BT)

WiFi 7 2x2 MU-MIMO

802.11be Tri-band 2.4GHz/5GHz/6GHz

2T2R Wi-Fi+BT 5.4 M.2 LGA Type 1620 module



Wi-Fi +Bluetooth Combo Solution M.2 Module

WNSQ-290BE(BT), first Qualcomm based WiFi-7 (802.11be) module in M.2 1620 LGA formfactor, running PCIe (Wifi) and USB (Bluetooth), with Tri-band capability (2.4GHz, 5GHz, and 6GHz). WNSQ-290BE(BT) supports single band operation on 2.4GHz with 5GHz, or 6GHz, and support IEEE802.11 be/ax/ac/a/b/g/n protocol, up to 160MHz mode.

WNSQ-290BE(BT) designed with 2 spatial streams (2T2R, or 2x2) in MU-MIMO mode. Adopting the latest 802.11be solution. WNSQ-290BE(BT) is ideal for next-generation high throughput

Software wise WNSQ-290BE(BT) support Linux (Open Source) in the near future. The module is capable to run on both x86 platform and ARM based platform, and supports STA mode and Soft AP Mode*, recommend to run on application includes: digital signage/POS, rugged computer / tablets, fanless automation PC and other industrial environment applications that requires high speed data transmission.

Embedded Application

Applications include IPC/ Advertising machine/ OTT/ IPTV/ DVB/ STB / DV/ Mini Driving Recorder/ Intelligent Projector Pico/ VR/ AR terminal/ POS machine/ Vehicle mounted front/ Rear Terminal UAV/ Robot/ Intelligent Gateway/ Smart city and other electronic products.

Key Feature

- Compliant with IEEE 802.11 be/ax/ac/a/b/g/n
- Support Multi-link Operation (MLO)
- Supports 2x2 Multi-User Multiple-Input Multiple-Output (MU-MIMO)
- Channel bandwidth up to 40 MHz at 2.4 GHz, up to 160 MHz at 5 GHz and 6 GHz
- Support tri-band 2.4 GHz/5 GHz/6 GHz
- Support up to 4096 QAM modulation at each band

Specification

Standards	IEEE 802.11be/ax/ac/a/b/g/n (2T2R) Bluetooth V5.4,V5.2, V5.1, V5.0, V4.2, V4.1, V4.0LE, V3.0, V2.1+EDR
Chipset	Qualcomm Atheros QCC2073
Data Rate	802.11b: 11Mbps 802.11a/g: 54Mbps 802.11n: MCS0~15 802.11ac: MCS0~9 802.11ax: MCS0~13 802.11be: MCS0~13 Bluetooth: 1 Mbps, 2Mbps and Up to 3Mbps
Operating Frequency	IEEE 802.11 be/ax/ac/a/b/g/n ISM Band, 2.412GHz~2.484GHz, 5.150GHz~5.850GHz, 5.925~7.125GHz *Subject to local regulations
Interface	WLAN: PCIe Bluetooth: USB or UART
Form Factor	M.2 LGA 1620
Antenna	2 x IPEX MHF4 connectors Main: WiFi Aux: WiFi/BT
Modulation	Wi-Fi: 802.11b: DSSS (DBPSK, DQPSK, CCK) 802.11g: OFDM (BPSK, QPSK, 16-QAM, 64-QAM) 802.11n: OFDM (BPSK, QPSK, 16-QAM, 64-QAM) 802.11a: OFDM (BPSK, QPSK, 16-QAM, 64-QAM) 802.11ac: OFDM (BPSK, QPSK, 16-QAM, 64-QAM, 256-QAM) 802.11ax: OFDMA (BPSK, QPSK, 16-QAM, 64-QAM, 256-QAM, 1024-QAM, 4096-QAM) 802.11be: OFDMA (BPSK, QPSK, 16-QAM, 64-QAM, 256-QAM, 1024-QAM, 4096-QAM) BT: Header: GFSK Payload 2M: $\pi/4$ -DQPSK Payload 3M: 8-DPSK

Power Consumption	11be TX mode: 802 mA (MAX) RX mode: 407 mA (MAX)
Operating Voltage	DC 3.3V
Operating Temperature Range	-10°C ~+65°C (Operating)
Storage Temperature Range	-40°C~85°C
Humidity (Non-Condensing)	5%~90% (Operating) 5%~90% (Storing)
Dimension L x W x H (in mm)	20mm(± 0.15mm) x 16 mm(± 0.15mm) x 1.7mm(± 0.3mm)
Weight (g)	1.5g
Driver Support	Linux (Open Source)
Security	64/128-bits WEP, WPA, WPA2, WPA3, 802.1x

OUTPUT POWER & SENSITIVITY
802.11b

Data Rate	Tx ± 1.5 dBm	Rx Sensitivity
11Mbps	18 dBm	≤ -91 dBm

802.11g

Data Rate	Tx ± 1.5 dBm	Rx Sensitivity
54Mbps	16 dBm	≤ -77.5 dBm

802.11n / 2.4GHz

HT20	Data Rate	Tx ± 1.5 dBm (1TX)	Tx ± 1.5 dBm (2TX)	Rx Sensitivity
	MCS7	16.5 dBm	19.5 dBm	≤ -76.5 dBm
HT40	MCS7	14.5 dBm	17.5 dBm	≤ -74 dBm

802.11a

Data Rate	Tx ± 2 dBm	Rx Sensitivity
54Mbps	16 dBm	≤ -76.5 dBm

802.11n / 5GHz

HT20	Data Rate	Tx ± 2 dBm (1TX)	Tx ± 2 dBm (2TX)	Rx Sensitivity
	MCS7	15dBm	18dBm	≤ -76 dBm
HT40	MCS7	14.5 dBm	17.5 dBm	≤ -73.5 dBm

802.11ac

VHT80	Data Rate	Tx ± 2 dBm (1TX)	Tx ± 2 dBm (2TX)	Rx Sensitivity
	MCS9	13 dBm	16 dBm	≤ -64.5 dBm
VHT160	MCS9	13 dBm	16 dBm	≤ -62 dBm

802.11ax / 2.4 GHz

HE40	Data Rate	Tx ± 1.5 dBm (1TX)	Tx ± 1.5 dBm (2TX)	Rx Sensitivity
	MCS13	11.5 dBm	14.5 dBm	≤ -55.5 dBm

802.11ax / 5GHz

HE80	Data Rate	Tx ± 2 dBm (1TX)	Tx ± 2 dBm (2TX)	Rx Sensitivity
	MCS13	12 dBm	15 dBm	≤ -52 dBm
HE160	MCS13	12 dBm	15 dBm	≤ -49.5 dBm

802.11ax / 6GHz				
HE80	Data Rate	Tx $\pm$ 2 dBm (1TX)	Tx $\pm$ 2 dBm (2TX)	Rx Sensitivity
	MCS13	11 dBm	14 dBm	$\leq$ -51 dBm
HE160	MCS13	11 dBm	14 dBm	$\leq$ -48.5 dBm

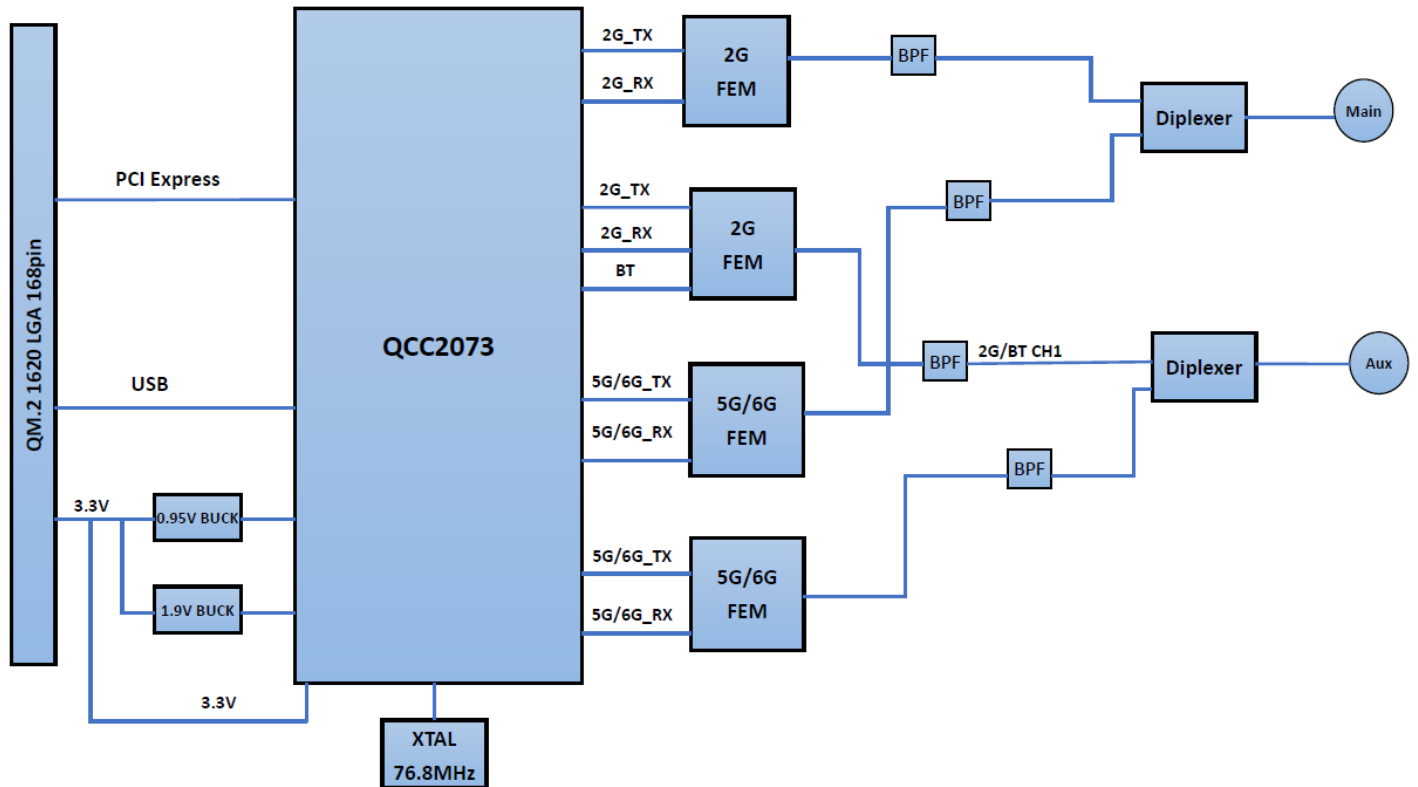
802.11be / 2.4 GHz				
EHT40	Data Rate	Tx $\pm$ 1.5 dBm (1TX)	Tx $\pm$ 1.5 dBm (2TX)	Rx Sensitivity
	MCS13	11.5 dBm	14.5 dBm	$\leq$ -56 dBm

802.11be / 5GHz				
EHT80	Data Rate	Tx $\pm$ 2 dBm (1TX)	Tx $\pm$ 2 dBm (2TX)	Rx Sensitivity
	MCS13	12 dBm	15 dBm	$\leq$ -52.5 dBm
EHT160	MCS13	12 dBm	15 dBm	$\leq$ -49.5 dBm

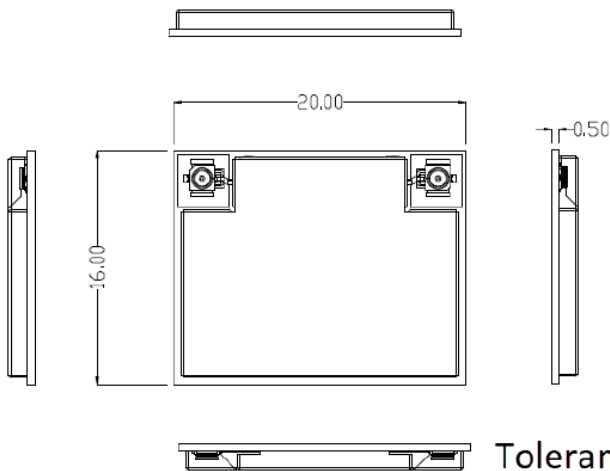
802.11be/ 6GHz				
EHT80	Data Rate	Tx $\pm$ 2 dBm (1TX)	Tx $\pm$ 2 dBm (2TX)	Rx Sensitivity
	MCS13	11 dBm	14 dBm	$\leq$ -51.5 dBm
EHT160	MCS13	11 dBm	14 dBm	$\leq$ -48.5 dBm

Bluetooth		
Data Rate	Tx $\pm$ 2dBm (Class I and Class II Device)	Rx Sensitivity
3M bps	11 $\leq$ Output Power $\leq$ 17 dBm	BER<0.1% Typical: -90dBm
1M bps	3 $\leq$ Output Power $\leq$ 7dBm	BLE 1Mbps (PER <30.8%) Typical: -93dBm
2M bps	3 $\leq$ Output Power $\leq$ 7dBm	BLE 2Mbps (PER <30.8%) Typical: -90dBm

Block Diagram



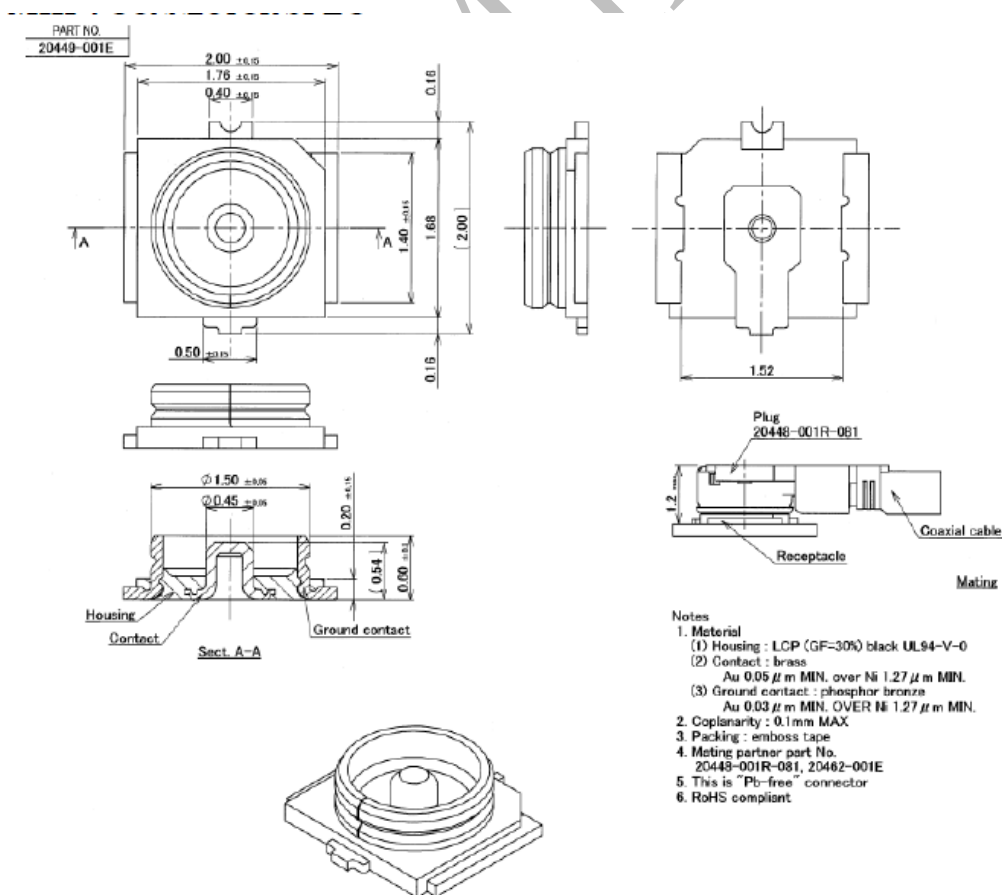
Mechanical Dimension (mm)



Tolerance: C

DIM	DEG	A	B	C	D	ANGLE
0-5		± 0.05	± 0.05	± 0.10		$0^\circ - 30^\circ \pm 0.1^\circ$
5-10		± 0.05	± 0.10	± 0.15		$31^\circ - 60^\circ \pm 0.3^\circ$
10-50		± 0.10	± 0.15	± 0.20		$61^\circ - 90^\circ \pm 0.5^\circ$
50-100		± 0.15	± 0.20	± 0.25		
100-		$\pm 0.15\%$	$\pm 0.20\%$	$\pm 0.25\%$		

MHF4 connector spec.



Module Land Pad

module pinout (Top View)

[illegible]

Pin Assignment

The following section illustrate signal pin-outs for the module connector.

* The following signal type is defined:

I: Input; O: Output; I/O: Input/Output; G: Ground; P: Voltage Supply

Pin#	Pin Name	Type	Description
1	NC	NC	No Connection
2	NC	NC	No Connection
3	NC	NC	No Connection
4	3.3V	P	VDD system power supply input
5	3.3V	P	VDD system power supply input
6	GND	G	Ground connections
7	WL_TX_EN	O	The QCC asserts this GPIO to high state, when either 5GHz chain0 or chain1 is set to transmit at power greater than certain power. When this GPIO is set high, the LAA receivers is placed in a protected state
8	LAA_TX_EN	I	This is an input from the SDR to QCC. The SDR sets GPIO high if LAA is transmitting above a prescribed RF power. GPIO is monitored by QCC. When it goes high, QCC places the 5GHz receiver in a protected state.
9	NC	NC	No Connection
10	NC	NC	No Connection
11	LTE_COEX_RXD	I	WSI interface for LTE co-existing interface with LTE modem to enable FW communication
12	LTE_COEX_TXD	O	WSI interface for LTE co-existing interface with LTE modem to enable FW communication
13	WLAN_DEVICE_SOL	O	Device sign of life
14	NC	NC	No Connection
15	NC	NC	No Connection
16	WLAN_HOST_SOL	I	HOST sign of life
17	GND	G	Ground connections
18	WL_DBG_UART_RXD	I	Debug interface (Host no need to connect)
19	WL_DBG_UART_TXD	O	Debug interface (Host no need to connect)
20	GND	G	Ground connections
21	HMT_PCIE_TXN1	O	PCIe TX differential signal lane 1 shall be connected to receiver differential pair on the system board
22	HMT_PCIE_TXP1	O	PCIe TX differential signal lane 1 shall be connected to

			receiver differential pair on the system board
23	GND	G	Ground connections
24	HMT_PCIE_RXN1	I	PCIe TX differential signal lane 1 shall be connected to transmitter differential pair on the system board
25	HMT_PCIE_RXP1	I	PCIe TX differential signal lane 1 shall be connected to transmitter differential pair on the system board
26	GND	G	Ground connections
27	32KHz_CLK	I	Suspend Clock is a 32.768KHz clock supply input that is provided by platform to enable WLAN to enter reduce power consumption mode. A pull-down resistor is required if this pin is not used.
28	W_DISABLE_L	I	It is an interrupt pin to WLAN. When WLAN detected interrupt, it turns off WLAN MAC/PHY/RF for power save application
29	PEWAKEO_L	O	WALN PCIe wake-up signal. It is an open-drain signal that requires an external pull-up
30	CLKREQO_L	I/O	WALN PCIe clock request signal. It is an open-drain signal that requires an external pull-up
31	PERSTO_L	I	WLAN PCIe reset
32	GND	G	Ground connections
33	HMT_PCIE_REFCLKN	I	WLAN PCIe reference clock input differential signals
34	HMT_PCIE_REFCLKP	I	WLAN PCIe reference clock input differential signals
35	GND	G	Ground connections
36	HMT_PCIE_TXN0	O	PCIe TX differential signal lane 0 shall be connected to receiver differential pair on the system board
37	HMT_PCIE_TXP0	O	PCIe TX differential signal lane 0 shall be connected to receiver differential pair on the system board
38	GND	G	Ground connections
39	HMT_PCIE_RXN0	I	PCIe RX differential signal lane 0 shall be connected to transmitter differential pair on the system board
40	HMT_PCIE_RXP0	I	PCIe RX differential signal lane 0 shall be connected to transmitter differential pair on the system board
41	GND	G	Ground connections
42	N79_TX_EN-	I	This is an input from the SDR to QCC. This GPIO is set high by the SDR when N79 transmits above a prescribed RF power. The QCC can be configured to respond to this GPIO when operating below a certain

			channel number, and to ignore this GPIO when operating above a certain channel number. When QCC responds to this GPIO, it places the 5GHz receivers in a protected state
43	WL_TXEN_TO_N79	O	This is an output from the QCC to SDR. The QCC asserts this GPIO to high state when the 5GHz or 6GHz chains are transmitting above certain power, and below a configurable channel frequency. The intent of allowing the channel to be configured is to improve concurrency with N79, depending on the filter selected and used on the device
44	SENS_TXD	O	Bluetooth 2-wire UART interfaces connect to the sensor block in the host
45	WL_EN	I	WLAN enable signal. It is an input, active high to enable WLAN operation
46	TMS	-	JTAG signal, no connect
47	TDO	-	JTAG signal, no connect
48	TCK	-	JTAG signal, no connect
49	TDI	-	JTAG signal, no connect
50	TRST	-	JTAG signal, no connect
51	NC	-	No Connection
52	NC	-	No Connection
53	BT_WAKEUP_HOST	O	Wakeup host via Bluetooth while receiving magic Packet frame
54	BT_UART_CTS	I	UART Clear to Send
55	BT_UART_TXD	O	UART Transmit Data. The baud rate is 115200 as default and can be configured up to 3.125Mbps
56	BT_UART_RXD	I	UART Receive Data. The baud rate is 115200 as default and can be configured up to 3.125Mbps
57	BT_UART_RTS	O	UART Ready to Send
58	BT_I2S_0_WS_GPIO	I	Bluetooth 4-wire I2S interface operated in slave mode. The clock rate is up to 2.048MHz. I2S word select
59	BT_I2S_0_SDI_GPIO	I	Bluetooth 4-wire I2S interface operated in slave mode. The clock rate is up to 2.048MHz. I2S data input
60	BT_I2S_0_SDO_GPIO	O	Bluetooth 4-wire I2S interface operated in slave mode. The clock rate is up to 2.048MHz. I2S data output

61	BT_I2S_0_SCK_GPIO	I	Bluetooth 4-wire I2S interface operated in slave mode. The clock rate is up to 2.048MHz. I2S continuous serial clock
62	GND	G	Ground connections
63	W_DISABLE2_L	I	It is an interrupt pin to BT. Active low to disable BT function
64	LED2	OD	BT status indicator
65	LED1	OD	Wi-Fi status indicator
66	VIO_1V8	P	1.8V source input for VDD_IO
67	WAKE_BT	I	Wakeup BT via HOST while sending an interrupt event
68	GND	G	Ground connections
69	BT_USB_DM	I/O	USB Data- differential serial data interface
70	BT_USB_DP	I/O	USB Data+ differential serial data interface
71	GND	G	Ground connections
72	3.3 V	P	VDD system power supply input
73	3.3 V	P	VDD system power supply input
74	GND	G	Ground connections
75	BT_USB_UART_SEL	O	Connect this pin to GND if platform is x86
76	IO_1V8_SEL	O	Connect this pin to GND if platform is x86
77~168	GND	G	Ground connections

Note:

X86: BT_USB_UART_SEL=GND

IO_1V8_SEL=GND

WOS: BT_USB_UART_SEL=Floating

IO_1V8_SEL=Floating

Certification (TBD)

☐ FCC

☐ IC

☐ NCC

☐ CE (RED EN 300 328 V2.2.2 / EN 301 893 V2.1.1)

☐ MIC

☐ ASNZS

Ordering Information

Product Name	Part Number	Description
WNSQ-290BE(BT)	R9701B10001	11be/ax/ac/a/b/g/n Tri-band, 2T2R Wi-Fi+BT M.2 1620 LGA Module

Optional Accessory(TBD)

Product Name	Part Number	Description
AD-510AX	R3410A10051	Dipole Antenna,2.27dBi/2.69dBi/3.52dBi/2.4G/5G/6GHz,RP-SMA(M)connector
CBIRF-NE150	R3470300025	RF Cable, I-PEXMHF4 to RP-SMA(F); L150mm; Coaxial 0.81 Black
CBIRF-NE250	R3470300026	RF Cable, I-PEXMHF4 to RP-SMA(F); L250mm; Coaxial 0.81 Black