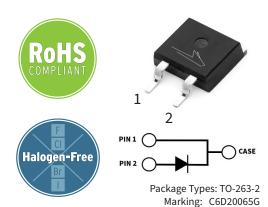


6th Generation 650 V, 20 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Features

Rev. 1, November 2023

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Low Leakage Current (I_p)

Applications

- Industrial Power Supplies
- Uninterruptible & AUX Power Supplies
- Switch Mode Power Supplies
- Solar Inverters
- Power Factor Correction
- Server/Telecom Power Supplies

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	650	.,			
DC Blocking Voltage	V _{DC}	650	V			
		64		T _J = 25 °C		
Continuous Forward Current	I _F	32		T _J = 125 °C	Fig. 3	
		20		T _J = 150 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	78	A	T _C = 25 °C, t _p = 10 ms, Half Sine Wave		
		45		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
Non-Repetitive Forward Surge Current	I _{FSM}	135		T _c = 25 °C, t _p = 10 ms, Half Sine Wave	Fig. 8	
		111		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
Non-Repetitive Peak Forward Surge Current	I _{F,Max}	1550		$T_{c} = 25 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
		1290		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
Power Dissipation	P _{tot}	163	W	T _J = 25 °C	Fig. 4	
		71		T _J = 110 °C		
i²t value	∫i²dt	91	A ² s	T _c = 25C, tp=10ms		
		62		T _c = 110C, tp=10ms		

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
- IV II	V	1.27	1.50	V	I _F = 20 A, T _j = 25 °C	Fig. 1
Forward Voltage	V _F	1.37	1.60		I _F = 20 A, T _j = 175 °C	
Reverse Current		5	30	μА	$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$	Fig. 2
	I _R	40	300		V _R = 650 V, T _j = 175 °C	
Total Capacitive Charge	Q _c	62		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		1153			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	120		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		96			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	9.5		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	0.76	°C/W	
Junction Temperature	T _j	-55 to +175		
Case & Storage Temperature	T _c	-55 to +175	,(

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Notes
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

Typical Performance

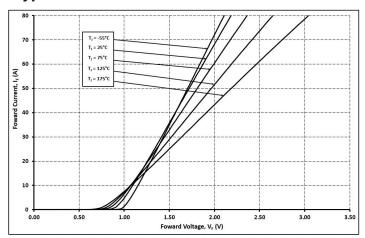


Figure 1Forward Characteristics

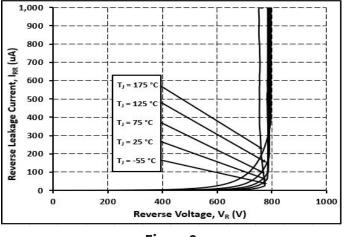


Figure 2Reverse Characteristics

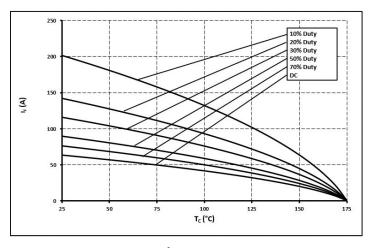
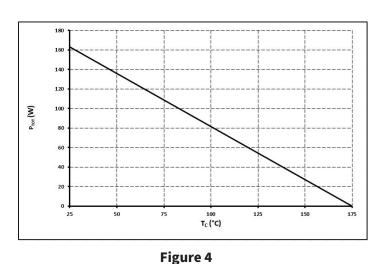
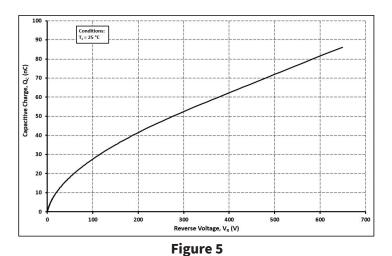


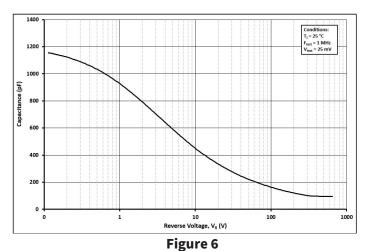
Figure 3Current Derating



Power Derating



Total Capacitance Charge vs. Reverse Voltage



Capacitance vs. Reverse Voltage

Typical Performance

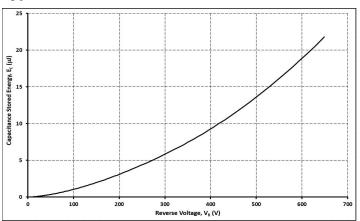
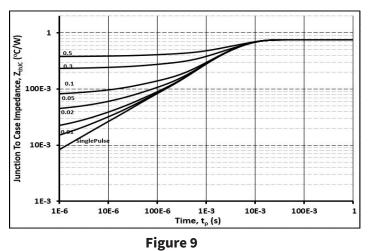
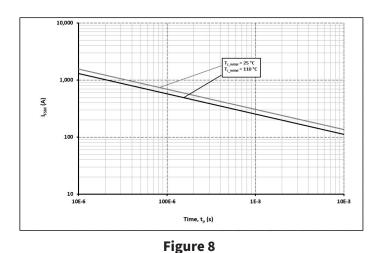


Figure 7Capacitance Stored Energy



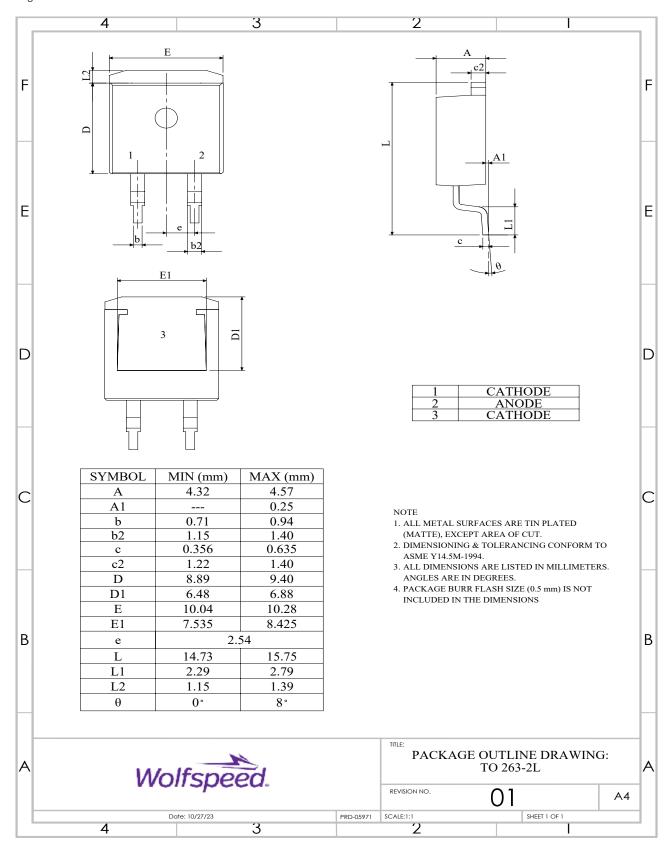
Transient Thermal Impedance



Non-Repetitive Peak Forward Surge Current vs. Pulse Duration

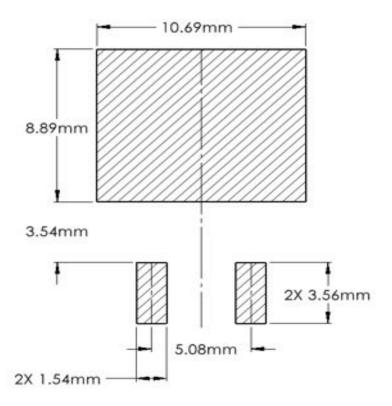
Package Dimensions & Pin-Out

Package: TO-263-2



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C6D20065G	Tube
C6D20065G-TR	Tape and Reel

REACh, RoHS, and Halogen-Free compliance documentation available for this product.

Revision History

Document Version	Date of Release	Description of Changes
0	March-2023	Initial Release
1	November-2023	Corrected Package Drawing L and L1

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